

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Seiji Funaba, et al. **Examiner:** Unassigned
Serial No: Unassigned **Art Unit:** Unassigned
Filed: Herewith **Docket:** 17072
For: SEMICONDUCTOR UNIT HAVING **Dated:** September 26, 2003
TWO DEVICE TERMINALS FOR
EVERY ONE INPUT/OUTPUT SIGNAL


Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CLAIM OF PRIORITY

Sir:

Applicants in the above-identified application hereby claim the right of priority in connection with Title 35 U.S.C. § 119 and in support thereof, will submit in due course a certified copy of Japanese Patent Application No. 281593/2002, filed on September 26, 2002 and Japanese Patent Application No. 103681/2003, filed on April 8, 2003.

Respectfully submitted,


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Dated: September 26, 2003

Paul J. Esatto, Jr.